

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

SELF-ALIGNED RAISED EXTRINSIC BASE BIPOLAR
TRANSISTOR STRUCTURE AND METHOD

Application Number :

Confirmation Number:

First Named Applicant: Alvin Joseph

Attorney Docket Number: BUR920030150US1







Art Unit:

Examiner:

Search string: (6559020 or 6531369 or 6472262 or 6080631 or 5266505 or 5117271 or 20030064555 or 20030057458 or 20020192894 or 20020132438).pn





US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6559020	2003-05-06	Salmi			
	2	6531369	2003-03-11	Ozkan et al.			
	3	6472262	2002-10-29	Chantre et al.			
	4	6080631	2000-06-27	Kitahata			
	5	5266505	1993-11-30	Ahlgren et al.			
	6	5117271	1992-05-26	Comfort et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030064555	2003-04-03	Ahlgren et al.			
	2	20030057458	2003-03-27	Freeman et al.			
	3	20020192894	2002-12-19	Kalnitsky et al.			
	4	20020132438	2002-09-19	Dunn et al.			

Signature

Examiner Name	Date
	12/20/04